

#5

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	§	
OLARIG, et al.	§	Group Art No.: To be Assigned
	§	
Serial No.: To be Assigned	§	
	§	Examiner: To be Assigned
Filed: DECEMBER 31, 2001	§	
	§	
Title: "NETWORK PROCESSOR INTERFACE SYSTEM"	§	Atty. Docket No.: 069099.0105
	§	

31011 U.S. PTO
10/039190
12/31/01

Honorable Commissioner for Patents
BOX PATENT APPLICATION
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R §§ 1.98(a)(1) and 1.98(a)(2)(i)**

Sir:

Pursuant to 37 C.F.R. §§ 1.98 (a)(1) and 1.98(a)(2)(i), Applicants submit herewith a copy of each of two patent applications being filed concurrently with the above-referenced patent application.

This application is related to co-pending patent applications serial number _____ [attorney docket number 069099.0106 / B6-A], entitled "Xon/Xoff Flow Control for Computer Network" filed December 31, 2001, by Hawkins Yao, John E. Jenne and Mark Lyndon Oelke, and serial number _____ [attorney docket number 069099.0107 / B6-B], entitled "Buffer to Buffer Credit Flow Control for Computer Network," filed December 31, 2001, by John E. Jenne, Mark Lyndon Oelke and Sompong Paul Olarig, both applications are hereby incorporated by reference herein for all purposes.

CERTIFICATE OF MAILING BY EXPRESS MAIL

PURSUANT TO 37 C.F.R. '1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE IS BEING DEPOSITED AS EXPRESS MAIL POST OFFICE TO ADDRESSEE WITH THE UNITED STATES POSTAL SERVICE IN AN ENVELOPE ADDRESSED TO:

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BOX PATENT APPLICATION
WASHINGTON, D.C. 20231

ON DECEMBER 31, 2001.

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Respectfully submitted,

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